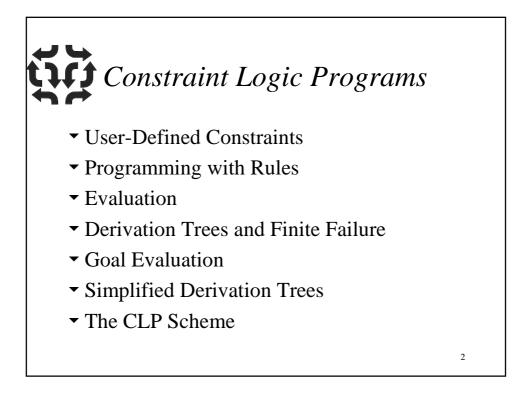
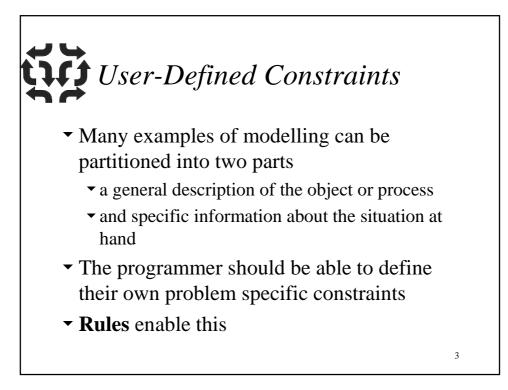
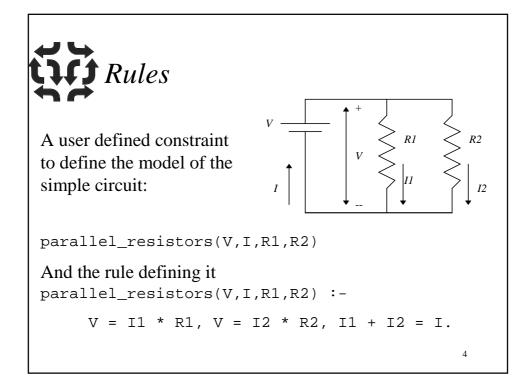
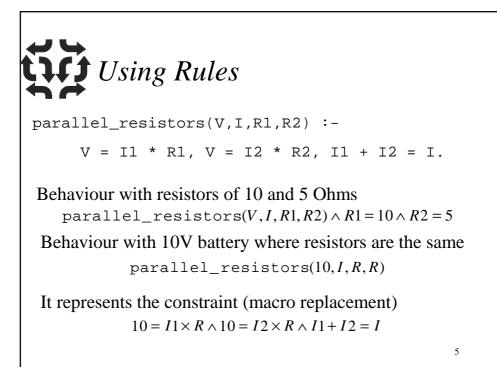
Chapter 4:Constraint Logic Programs

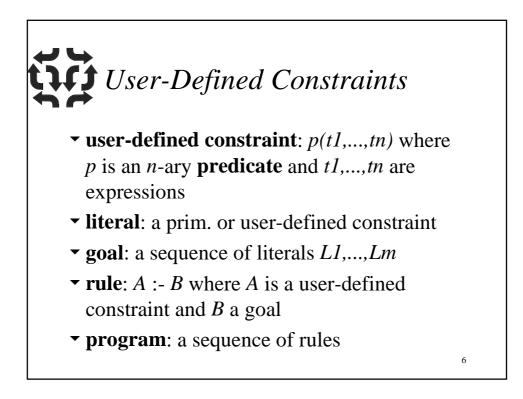
Where we learn about the only programming concept rules, and how programs execute

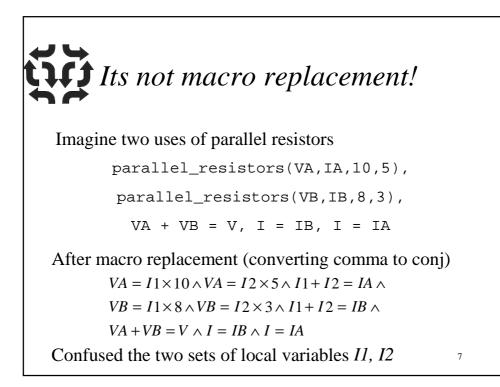


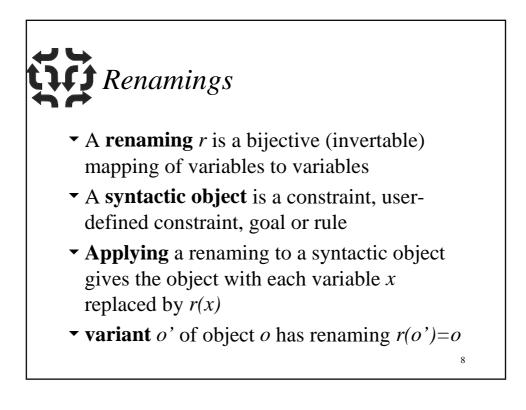


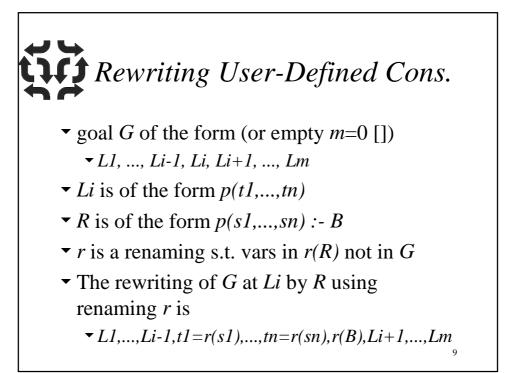








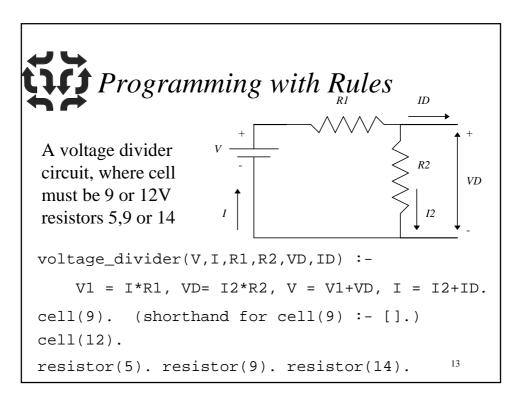


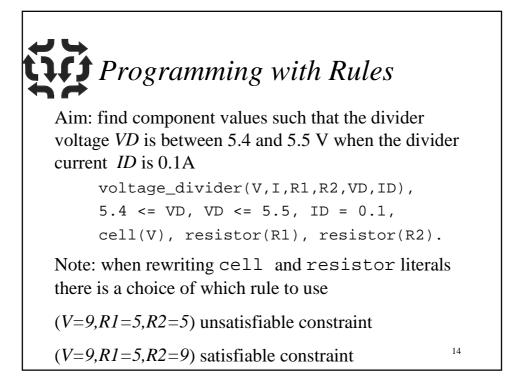


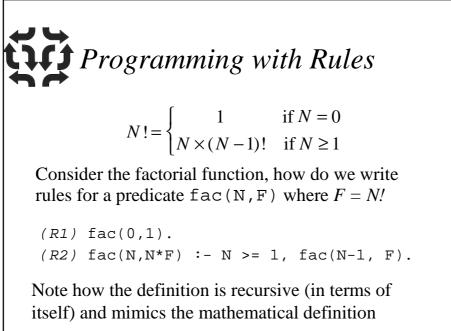
Parallel_resistors(VA, IA, 10, 5),
parallel_resistors(VA, IA, 10, 5),
parallel_resistors(VB, IB, 8, 3),
VA + VB = V, I = IB, I = IA
Rewrite the first literal with rule
parallel_resistors(V, I, R1, R2) :-
V = I1 * R1, V = I2 * R2, I1 + I2 = I.
Renaming: {
$$V \mapsto V', I \mapsto I', R1 \mapsto R1', R2 \mapsto R2', I1 \mapsto I1', I2 \mapsto I2'$$
}
parallel_resistors(V', I', R1', R2') :-
V' = I1'*R1', V' = I2'*R2', I1'+I2' = $\frac{10}{1'}$.

```
VA=V', IA=I', 10=R1', 5=R2', V' = I1'*R1', V' = I2'*R2', I1'+I2' = I', parallel_resistors(VB, IB, 8, 3), VA + VB = V, I = IB, I = IA
Rewrite the 8th literal
Renaming:{V \mapsto V'', I \mapsto I'', R1 \mapsto R1'', R2 \mapsto R2'', I1 \mapsto I1'', I2 \mapsto I2''
parallel_resistors(V', I'', R1'', R2'') :-
V''=I1''*R1'', V''=I2''*R2'', I1''+I2''^HI''.
```

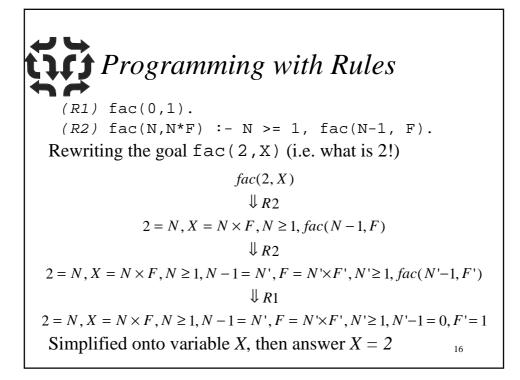
$$VA=V', IA=I', 10=R1', 5=R2',$$
$$V' = I1'*R1', V' = I2'*R2', I1'+I2' = I',$$
$$VB=V'', IB=I'', 8=R1'', 3=R2'',$$
$$V'=I1'*R1'', V'=I2''*R2'', I1''+I2''=I''$$
$$VA + VB = V, I = IB, I = IA$$
Simplifying onto the variables of interest V and I
$$V = 26/3 \times I$$







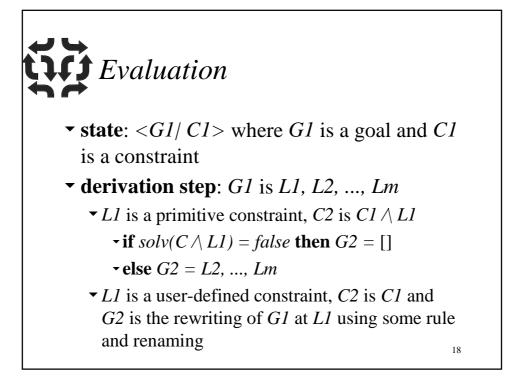
15

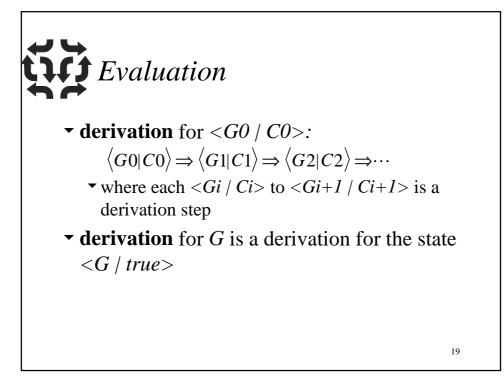


Evaluation

- In each rewriting step we should check that the conjunction of primitive constraints is satisfiable
- derivation does this
- ▼ in each step a literal is handled
 - primitive constraints: added to constraint store
 - ▼ user-defined constraints: rewritten

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$$\begin{array}{c} & & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & \\ & & & \\$$

